

United States Patent and Trademark Office



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO		
10/039,765	11/07/2001	Franck Roche	00RO30454288	9186		
27975	7590 08/25/2006		EXAM	EXAMINER.		
ALLEN, DYER, DOPPELT, MILBRATH & GILCHRIST P.A.			PATEL, NIMESH G			
1401 CITRUS CENTER 255 SOUTH ORANGE AVENUE P.O. BOX 3791		ART UNIT	PAPER NUMBER			
ORLANDO, FL 32802-3791			2112			
			DATE MAILED: 08/25/2006			

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/039,765	ROCHE ET AL.		
Examiner	Art Unit		
Nimesh G. Patel	2112		

	Nimesh G. Patel	2112	
The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence add	ress
THE REPLY FILED <u>14 August 2006</u> FAILS TO PLACE THIS AF	PPLICATION IN CONDITION FOR	ALLOWANCE.	
1. The reply was filed after a final rejection, but prior to or on this application, applicant must timely file one of the follow places the application in condition for allowance; (2) a No a Request for Continued Examination (RCE) in compliance time periods:	ving replies: (1) an amendment, aff tice of Appeal (with appeal fee) in o	idavit, or other evider compliance with 37 C	nce, which FR 41.31; or (3)
a) The period for reply expires 3 months from the mailing date			
b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire la	ater than SIX MONTHS from the mailin	g date of the final rejecti	on.
Examiner Note: If box 1 is checked, check either box (a) or (TWO MONTHS OF THE FINAL REJECTION. See MPEP 70	06.07(f).		
Extensions of time may be obtained under 37 CFR 1.136(a). The date have been filed is the date for purposes of determining the period of extunder 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	tension and the corresponding amount shortened statutory period for reply orig than three months after the mailing da	of the fee. The approprinally set in the final Offi	iate extension fee ce action; or (2) as
 The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exter a Notice of Appeal has been filed, any reply must be filed 	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of th	
AMENDMENTS 3. The proposed amendment(s) filed after a final rejection, I	out prior to the date of filing a brief,	will not be entered b	ecause
(a) They raise new issues that would require further co		TE below);	
(b) ☐ They raise the issue of new matter (see NOTE belo(c) ☐ They are not deemed to place the application in bet	• •	ducing or simplifying	the issues for
appeal; and/or (d) ☐ They present additional claims without canceling a €	corresponding number of finally rei	ected claims.	
NOTE: (See 37 CFR 1.116 and 41.33(a)).	somespending names or imany rej	000000000000000000000000000000000000000	
4. The amendments are not in compliance with 37 CFR 1.13	21. See attached Notice of Non-Co	mpliant Amendment	(PTOL-324).
5. Applicant's reply has overcome the following rejection(s)			
 Newly proposed or amended claim(s) would be al non-allowable claim(s). 	lowable if submitted in a separate,	timely filed amendme	ent canceling the
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is provided that the status of the claim(s) is (or will be) as follows: Claim(s) allowed: Claim(s) objected to: Claim(s) rejected:		ll be entered and an e	explanation of
Claim(s) withdrawn from consideration: AFFIDAVIT OR OTHER EVIDENCE			
 The affidavit or other evidence filed after a final action, bu because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e). 			
 The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to of showing a good and sufficient reasons why it is necessary 	vercome <u>all</u> rejections under appe y and was not earlier presented. S	al and/or appellant fa ee 37 CFR 41.33(d)(ils to provide a 1).
10. ☐ The affidavit or other evidence is entered. An explanatio REQUEST FOR RECONSIDERATION/OTHER	n of the status of the claims after e	ntry is below or attacl	ned.
 The request for reconsideration has been considered bu <u>See Continuation Sheet.</u> 	t does NOT place the application is	n condition for allowa	nce because:
12. Note the attached Information Disclosure Statement(s).	(PTO/SB/08 or PTO-1449) Paper N	lo(s)	
13. Other:			

Continuation of 11. does NOT place the application in condition for allowance because: In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See In re Van Geuns, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993). The I2C Specification discloses a method of transmitting data between two devices via a clock line(Figure 2, SCL) and at least one data line(Figure 2, SDA), the clock line being maintained by default on a first logic value(Page 8, Section 5; HIGH), the method comprising: providing each device with the ability to tie the clock line to a potential representing a second logic value(Page 8, Section 5; LOW; Figure 6; Both master and slave can tie the clock LOW) opposite the first logic value; tying the clock line to the second logic value, via the two devices, when data is transmitted(Page 9, Section 6.2); maintaining the tie to the clock line by the device to which the data is sent while the device has not read the data; and maintaining the data on the data line by the device sending the data at least until an instant when the clock line is released by the device to which the data is sent(Page 10, Section 7.1; Page 13, Section 8.3). The specification states that data is only allowed to change when clock is low. This means that the master sends a data bit while clock is low and maintains the bit even after the clock is released(Figure 4). Therefore, applicant's argument is not persuasive.

SUPERVISORY PATENT EXAMINER

SUPERVISORY PATENT EXAMINER